

In the Claims:

Please amend the Claims as follows:

1. (Currently Amended) An integrated circuit device comprising:

a first port for inputting write data and outputting read data; and

a second port for only inputting write data,

wherein at least one of the first port and the second port is selected by an external command when the write data is input.

2. (Currently Amended) The integrated circuit device of Claim 1 wherein the

second port has $4/2n-1/2^n$ times the number of pins of the first port, where n is a positive integer.

3. (Currently Amended) The integrated circuit device of Claim 1 wherein both the

first port and the second port are selected by an external command when the write data is input.

4. (Original) The integrated circuit device of Claim 1, further comprising a control

pin for receiving a predetermined control signal to select at least one of the first port and the second port.

5. (Currently Amended) An integrated circuit system comprising:

an integrated circuit device that includes a first port for inputting write data and outputting read data and a second port for only inputting write data; and a controller for generating a command to select either the first port or the second port.

6. (Currently Amended) The integrated circuit system of Claim 5 wherein the second port has $1/2n-1/2^n$ times the number of pins of the first port, where n is a positive integer.

7. (Currently Amended) The integrated circuit device of Claim 5 wherein both the first port and the second port are selected by the command when the write data are input.

8. (Original) The integrated circuit device of Claim 5 wherein the integrated circuit device further comprises a control pin that receives a predetermined control signal for selecting either or both of the first port and the second port.

9. (Currently Amended) An integrated circuit device comprising:
a first port for inputting write data and outputting read data;
a first buffering unit in signal communication with the first port for buffering and storing the input write data or output read data;
a second port for only inputting write data;

a second buffering unit in signal communication with the second port for buffering and storing the input write data; and

a selecting unit for selecting outputs from at least one of the first buffering unit and the second buffering unit to output in response to a selection signal, wherein at least one of the first port and the second port is selected by an external command when the write data is input and at least one of the first buffering unit and the second buffering unit is turned on by the external command.

10. (Currently Amended) The integrated circuit device of Claim 9 wherein the second port has $1/2n-1/2^n$ times the number of pins of the first port, where n is a positive integer.

11. (Currently Amended) The integrated circuit device of Claim 9 wherein the first buffering unit comprises:

an input/output buffer for receiving the write/read data input to or output from the first port, respectively; and

an input/output register for storing the write/read data output from the input/output buffer, respectively, and outputting the stored write/read data to the selecting unit.

12. (Currently Amended) The integrated circuit device of Claim 9 wherein the second buffering unit comprises:

an input buffer for receiving the write data input from the second port; and
an input register for storing the write data from the input buffer and outputting the
stored data to the selecting unit.

13. (Original) The integrated circuit device of Claim 9 wherein the selection signal
is generated from a command provided to the integrated circuit device.

14. (Currently Amended) The integrated circuit device of Claim 9 wherein both
the first port and the second port are selected by the external command when the write
data are input.

15. (Original) The integrated circuit device of Claim 9, further comprising a
control pin for receiving a predetermined control signal to select at least one of the first
port and the second port.

16. (Currently Amended) An integrated circuit device comprising:
a first port for inputting write data and outputting read data;
an input/output buffer for receiving the write data input to or read data output
from the first port;
a second port for only inputting write data;
an input buffer for receiving the write data input from the second port; and

a register for storing and outputting the write/read data from the input/output buffer, respectively, and from the input buffer wherein at least one of the first port and the second port is selected by an external command when the write data is input and at least one of the input/output buffer and the input buffer is turned on by the external command.

17. (Currently Amended) The integrated circuit device of Claim 16 wherein the second port has $1/2n-1/2^n$ times the number of pins of the first port, where n is a positive integer.

18. (Currently Amended) The integrated circuit device of Claim 16 wherein the register selectively outputs the write/read data output from the input/output buffer, respectively, or from the input buffer in response to the selection signal generated from a command provided to the integrated circuit device.

19. (Currently Amended) The integrated circuit device of Claim 16 wherein both the first port and the second port are selected by the external command when the write data are input.

20. (Original) The integrated circuit device of Claim 16 wherein the integrated circuit device further comprises a control pin for receiving a predetermined control signal to select at least one of the first port and the second port.

21. (New) The integrated circuit device of Claim 2 wherein n is at least 2.